DiSTERaP

Distributed Simulation Test Environment for Rapid Prototyping

Nazario Tancredi





Distributed Simulation Test Environemnt for Rapid Prototyping



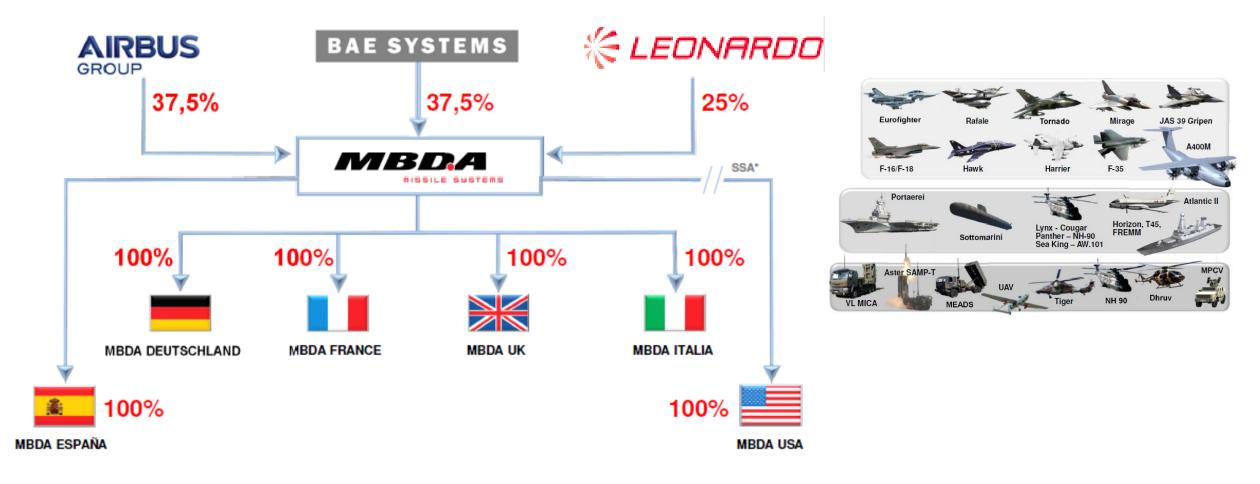
MISSILE SUSTEMS

Nazario Tancredi System Engineer. Simulation & Modelling Practitioner Missile Dynamics Italy. Simulation & Modelling MBDA Italy

31/05/2018

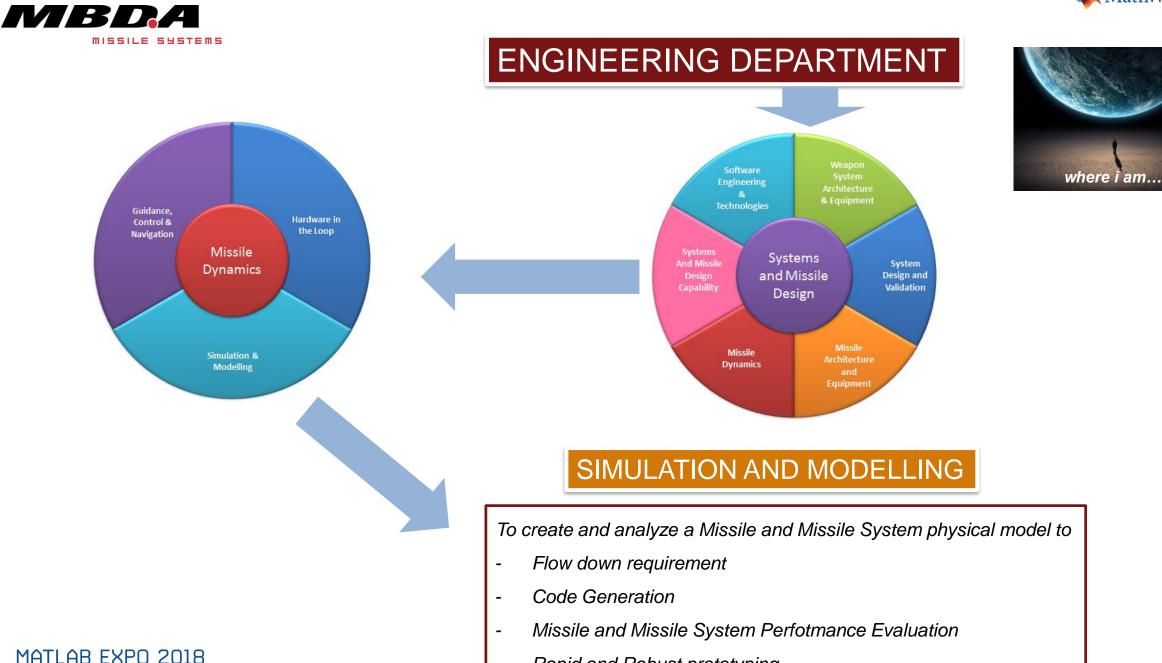






Development and production of Full Missile System





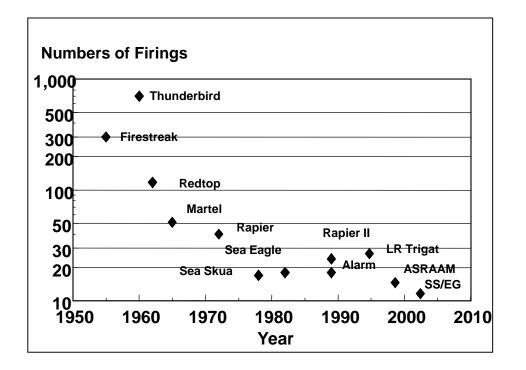
- Rapid and Robust prototyping



MBDA

To help in understanding the structure and behaviour of the system

- To help in designing the system before we commit to building it
- To understand product behaviour (both desirable and undesirable)
- To help in trade-off studies
- To help in developing specifications
- To help in product integration and testing
- To help in planning trials
- To help in training in the use of the system
- To help quantify system performance
- To help in demonstrating that we have met the customer requirements

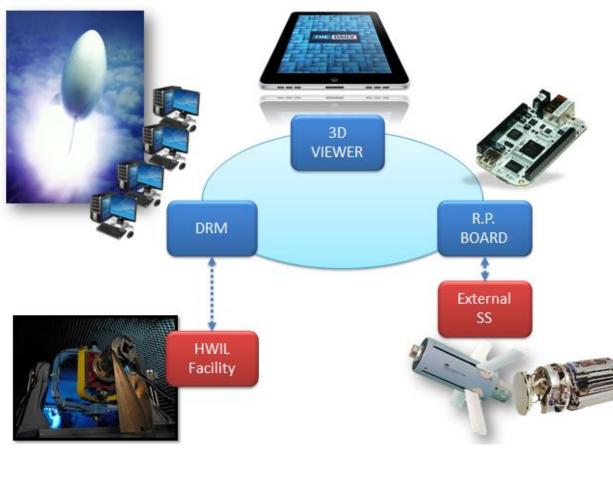


- To help in specifying, developing and proving software to be developed for the system
- To understand the performance of the system in scenarios where we cannot do trials, for example:
 - What is the probability of survival of a ship?
 - Will a faulty missile hit the launch aircraft?
 - What is the system performance in heavy countermeasures?





Distributed Simulation Test Environment for Rapid Prototyping



To have



a distributed

simulation architecture

to support

the rapid development

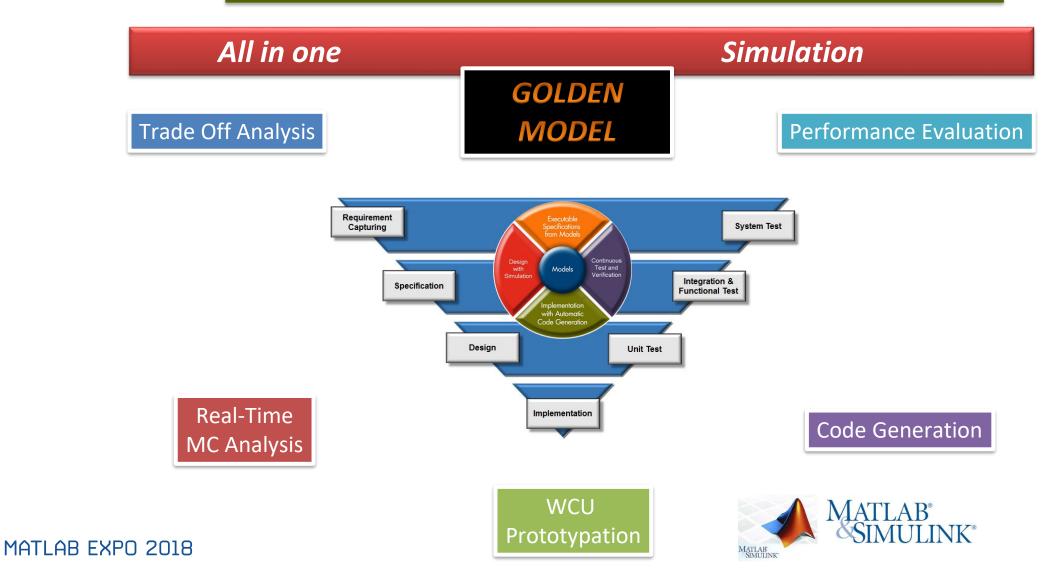
and the testing

of future missile systems

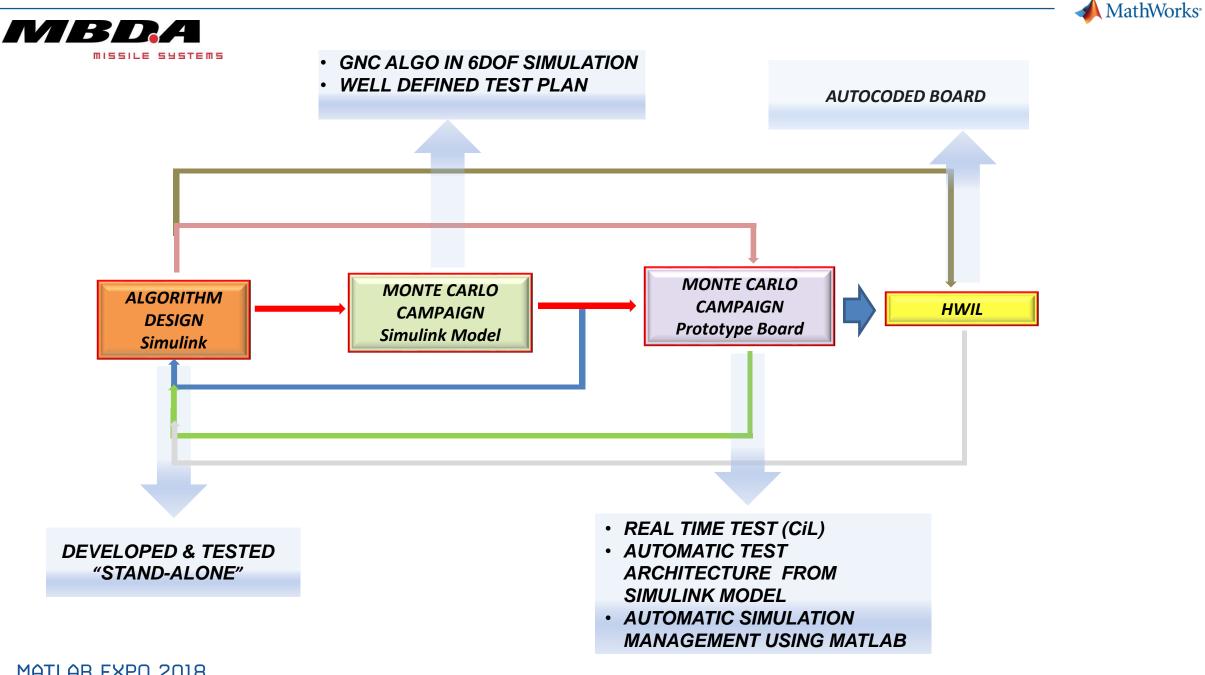




MBD-A Model-Based Design Approach



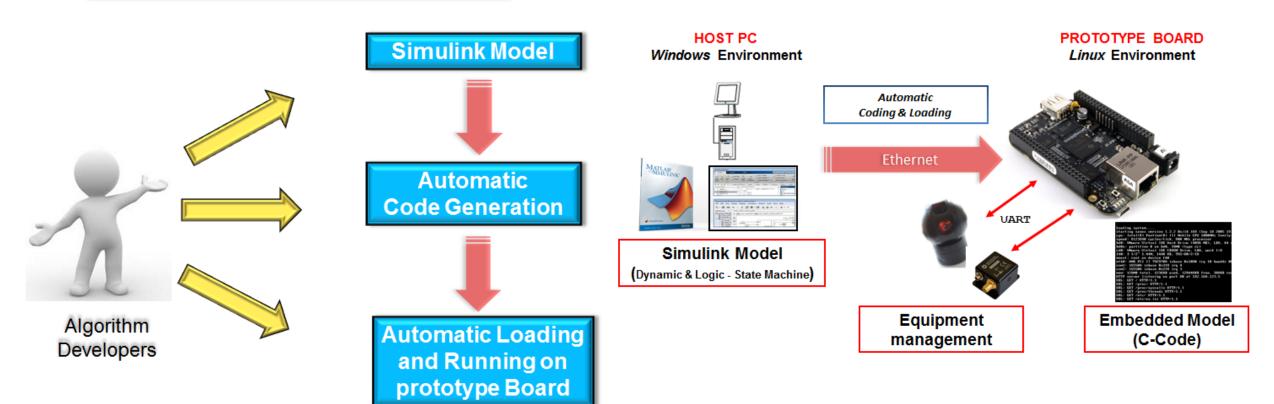
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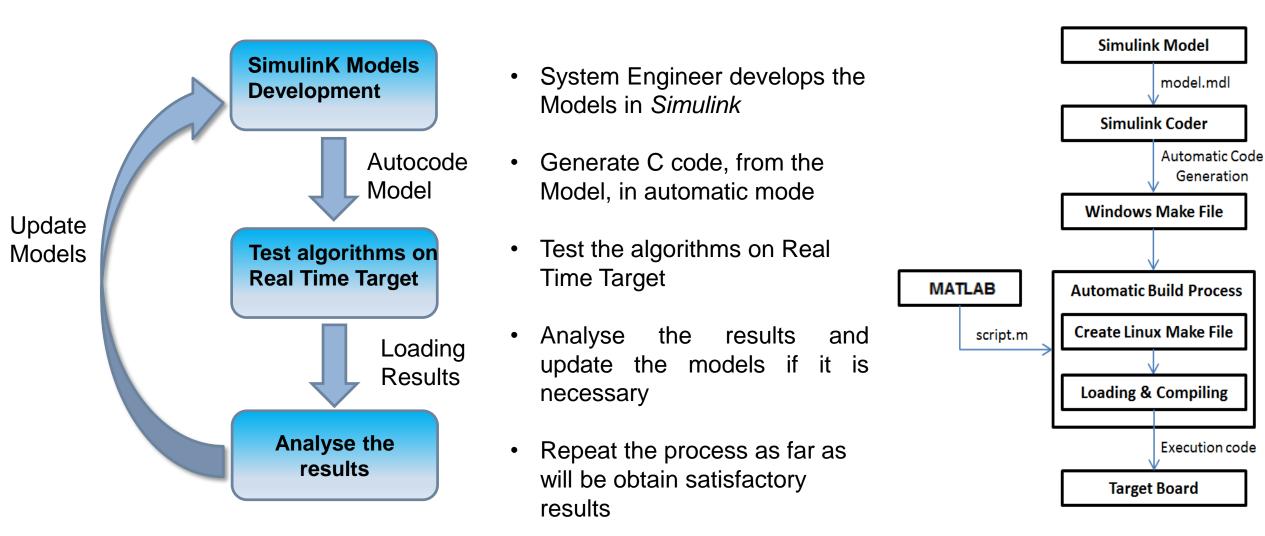


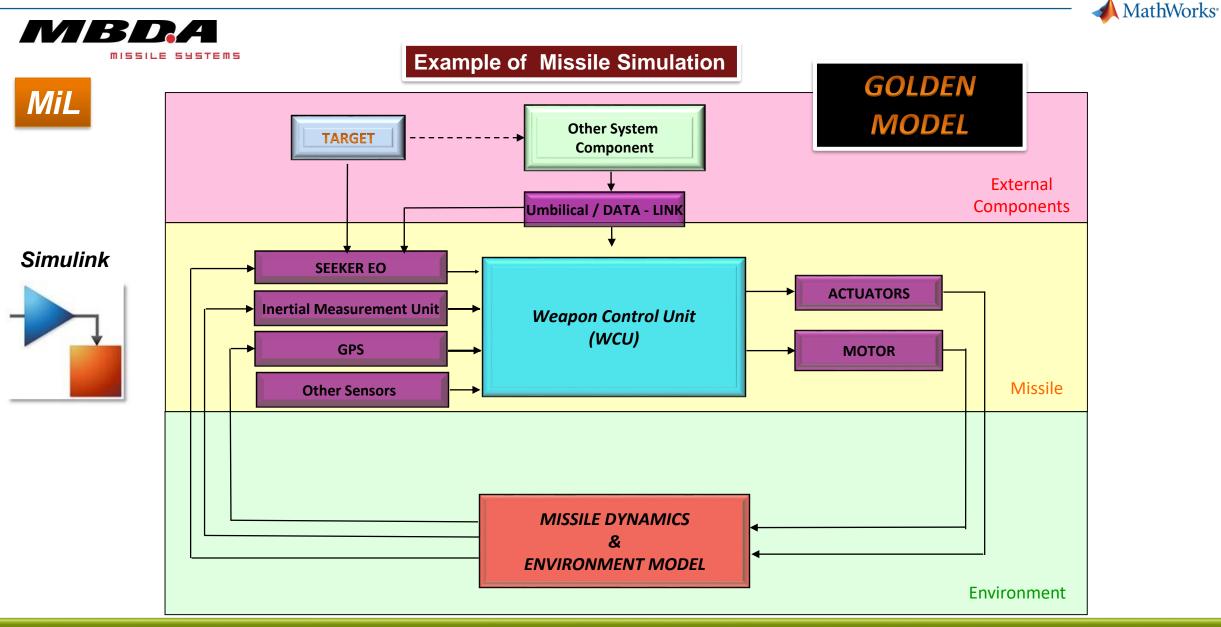
RAPID & ROBUST prototyping





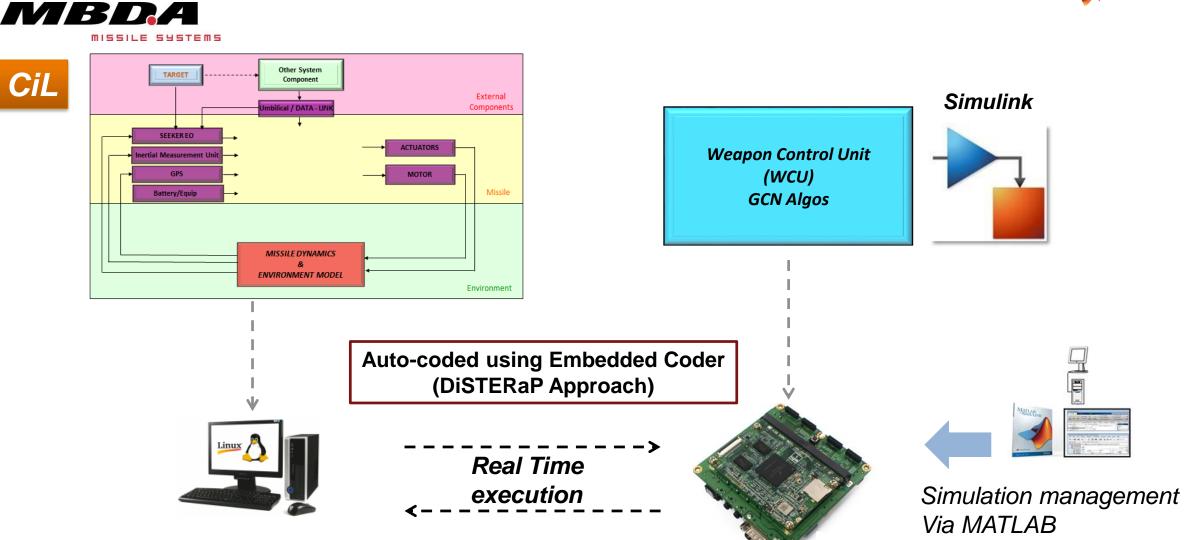






Guidance Navigation and Control algorithms performance evaluation in simulation (MiL – Model in the Loop)



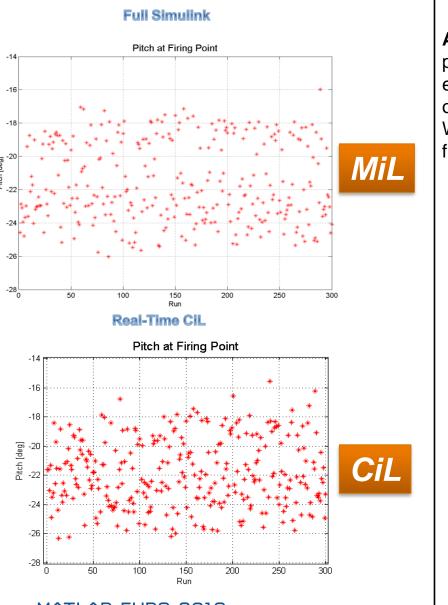


Guidance Navigation and Control algorithms performance evaluation on WCU prototype (CiL – Computer in the Loop)



HW Analysis performed in early phase of Project



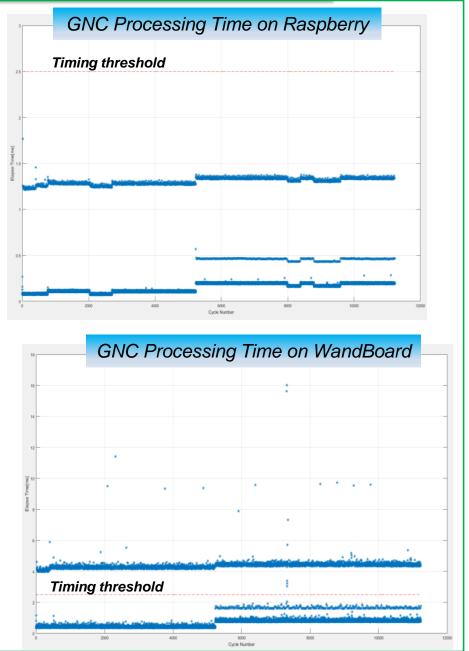


MATLAB EXPO 2018

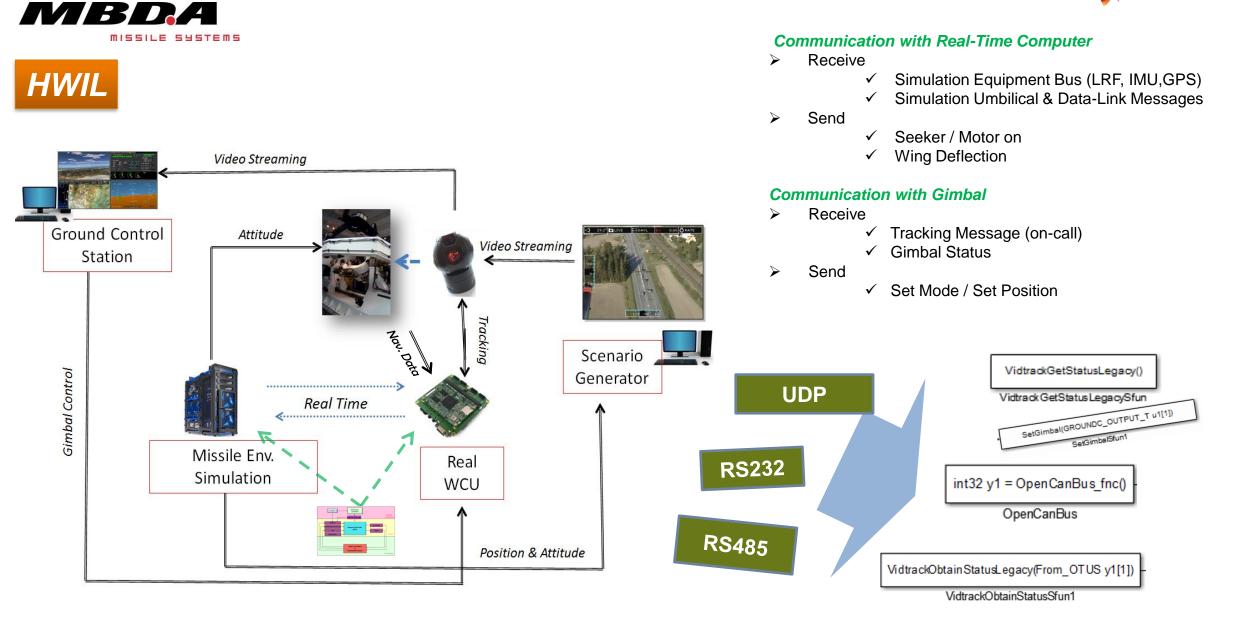
A. GCN Algorithms performance evaluation directly on prototype of WCU made in the first phase of design

> RAPID ON BOARD ALGORITHMS PERFORMANCE EVALUATION

B. GCN Algorithms performance evaluation directly on different HW with comparison purpose



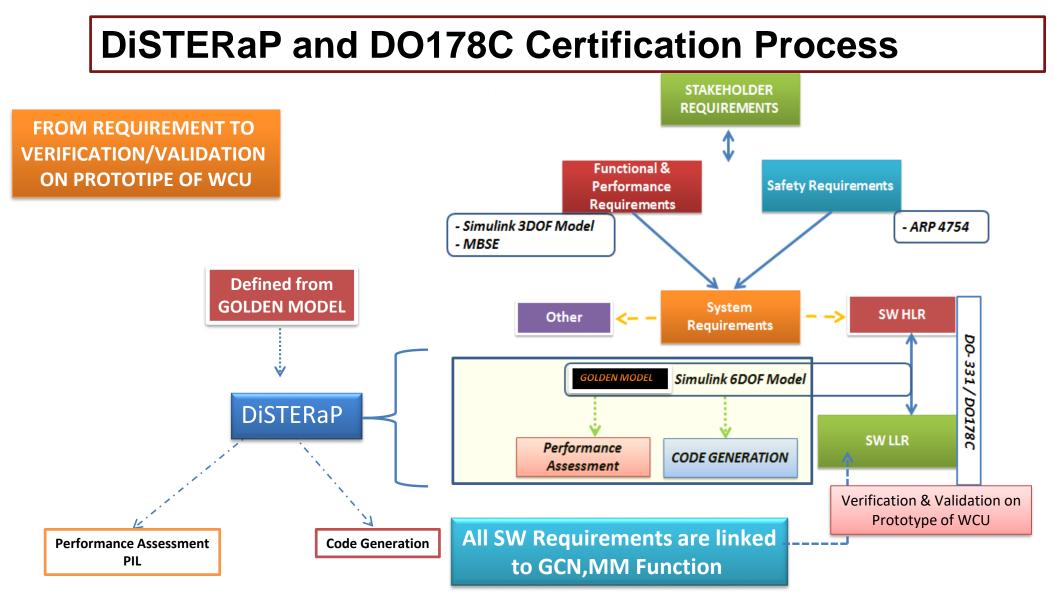


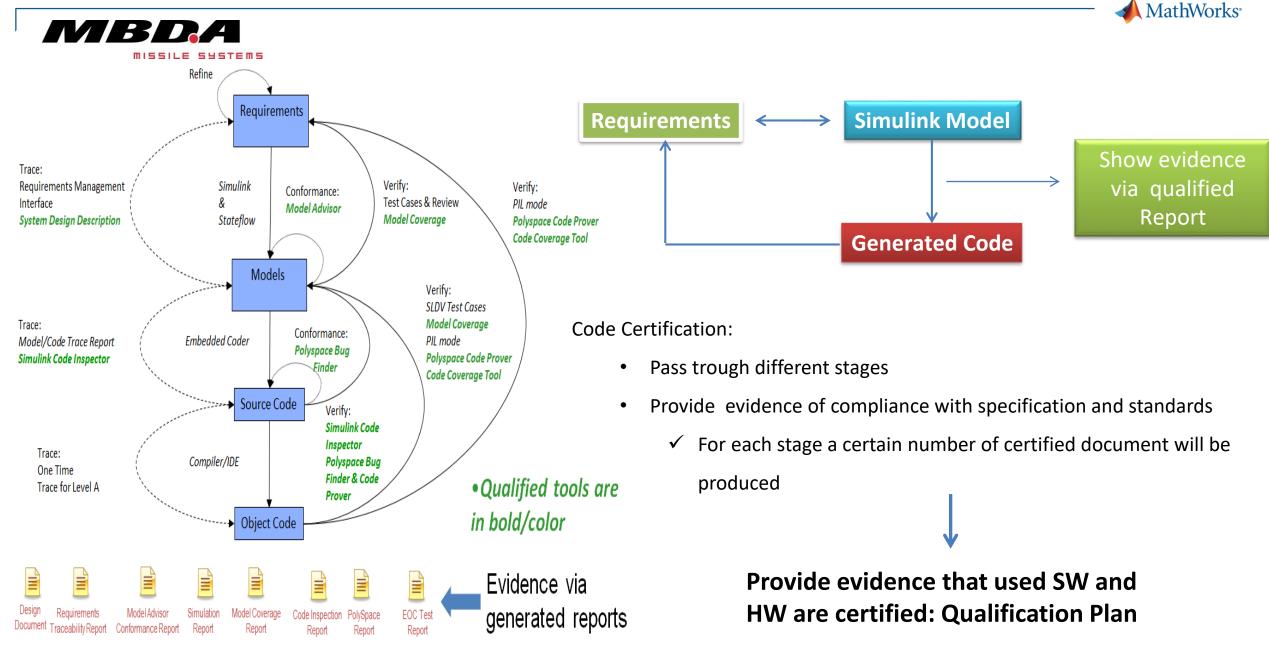


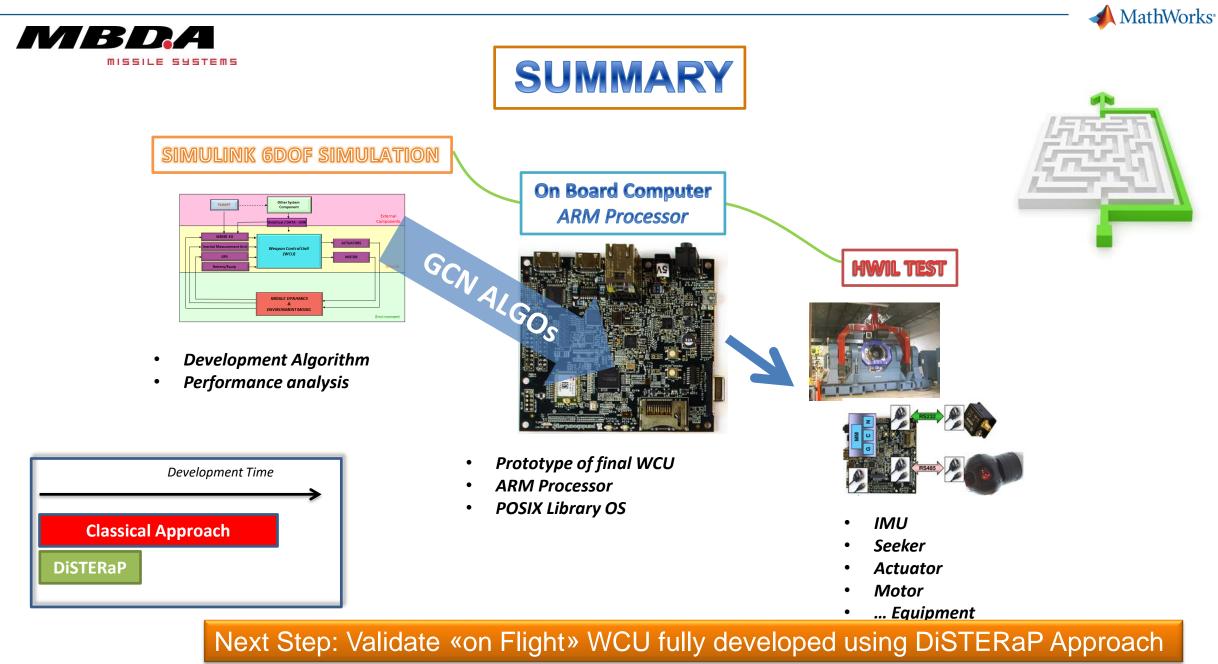
SW INTERFACE EMBEDDED IN SIMULINK MODEL USING LEGACY CODE TOOL



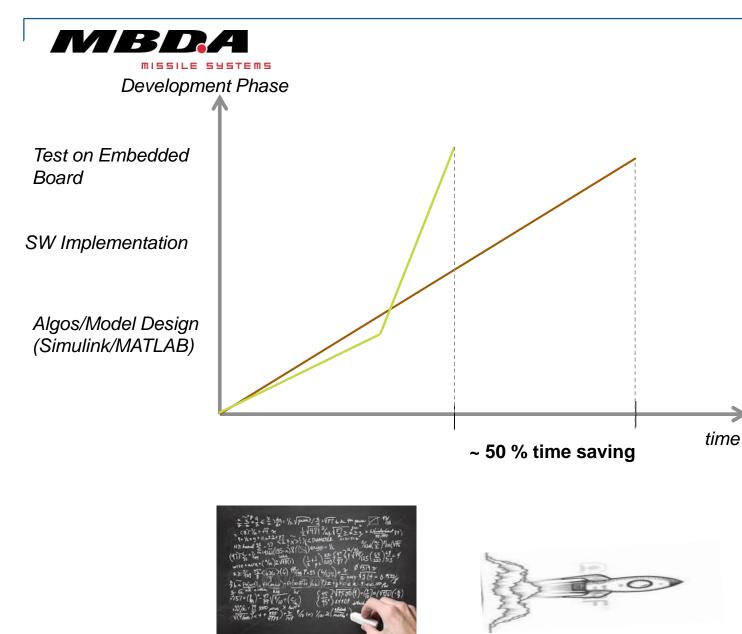












ADD:

• Robusteness to design: GCN

Algorithms development considering HW performance

REDUCE

- V&V Time
- Time to market





Key Takeaways

- 1. Models established as golden reference companywide
- 2. Time-consuming programming tasks eliminated
- 3. Simulations and analysis accelerated

USE OF «GOLDEN MODEL» AS A KEY DRIVE FACTOR IN YOUR DESIGN, IN FULL PRODUCT DEVELOPMENT PHASE







Nazario Tancredi

MBDA Italia S.p.A. Missile Dynamics Italy. Simulation & Modelling.Missile Trades. Via Carciano n. 4-50, 60-70 00131 Rome, Italy. Phone: +39 06 8771 5125. Mobile : +39 3385024166 Fax: +39 06 8771 3714.

MESD

RIDELE EVETER